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Applicant: Hofmeister, et al. Serial No.: 10/695,342

Filing Date: October 28, 2003 Group: 2828

Att'y Docket No.: 15436.253.66.1

For: TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE



INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

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Hofmeister, et al. Applicant: Serial No.: 10/695,342

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TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT For:

AND METHOD FOR FIBER OPTICS DEVICE

Examiner:	100	Date Considered:	1/0/07
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For:

TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT

AND METHOD FOR FIBER OPTICS DEVICE

Examiner:	w	Date Considered:	1/1/07
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October 28, 2003

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TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT AND METHOD FOR FIBER OPTICS DEVICE

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Examiner:	W	Date Considered:	1/1/02
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<u>NU</u> 88	2005/0058455	03/17/2005	Hosking et al.
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Sheet 7 of 9 Form PTO-1449 Applicant: Hofmeister, et al. 10/695,342 Att'y Docket No.: 15436.253.66.1 Serial No.: Group: 2828 Filing Date: October 28, 2003 TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT For: AND METHOD FOR FIBER OPTICS DEVICE JP 58140175 A (abstract) 08/19/1983 Japan 104 JP 62124576 A (abstract) 06/05/1987 Japan. JP 62235975 A (abstract) 10/16/1987 Japan 106 JP 62281485 A (abstract) Japan 12/07/1987 Other Documents (including author, title, pertinent pages, etc.) Examiner Initial* Yi Cai et al., "Jitter testing for gigabit serial communication transceivers," Jan -Feb 2002, IEEE Design and Test of Computers, Vol. 19, Issue 1, pp 66-74. MAEDA, Noriyuki "Notification of Reason(s) for Refusal," Japanese Patent 108 Application No. JP2002-563630, Nakamura, M. et al., July 13, 2005. Finisar Corp., "App. Note AN-2025: Using the Finisar GBIC I²C Test Diagnostics 109 Port," 1998. Hausdorf, Reiner, "Mobile Transceiver Measurements with Radiocommunication 110 Service Monitor CMS," News from Rohde & Schwarz, 127, IV, 1989, pp 4-7. Webopedia: The 7 Layers of the OSI Model [online] [retrieved 10/15/03]. 111 Retrieved from Internet: URL: http://webopedia.internet.com/quick ref/OST Layers.asp Webopedia.com: Public-Key Encryption [online] [retrieved 10/15/03]. Retrieved from Internet: URL: http://www.webopedia.com/TERM/p/public key cryptography.html

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Form PTO-1449 Sheet 8 of 9 Hofmeister, et al. Applicant: 10/695,342 Serial No.: Att'y Docket No.: 15436.253.66.1 Group: 2828 Filing Date: October 28, 2003 TEMPERATURE AND JITTER COMPENSATION CONTROLLER CIRCUIT For: AND METHOD FOR FIBER OPTICS DEVICE DV 113 Webopedia.com: MAC Address [online] [retrieved 10/15/03]. Retrieved from Internet: URL: http://www.webopedia.com/TERM/M/MAC address.html 114 Webopedia.com: 12C [online] [retrieved 11/11/03]. Retrieved from Internet: URL: http://www.webopedia.com/TERM/I/12C.html 115 Manchester Encoding [online] [retrieved 11/12/03]. Retrieved from Internet: URL: http://www.erg.abdn.ac.uk/users/gorry/course/phypages/man.html Documentation entitled "IR Receiver ASSP: T2525", copyright 2003 by Atmel 116 Corporation 117 Documentation entitled "IR Receiver for Data Communication: U2538B", copyright 2003 by Atmel Corporation 118 Documentation entitled "Low-Voltage Highly Selective IR Receiver IC: T2527", copyright 2002 by Atmel Corporation 119 Documentation entitled "Application Note: T2525/26/27", copyright 2003 by **Atmel Corporation** 120 LXT16706/16707 SerDes Chipset, Intel Products, www.intel.com/design/network/products/optical/phys/1xt16706.htm, April 19, 2002. 121 LXT35401 XAUI-to-Quad 3.2G Transceiver, Intel Products, www.intel.com/design/network/products/optical/phys/1xt35401.htm, April 19, 2002 Texas Instruments User's Guide, TLK2201 Serdes EVM Kit Setup and Usage, 122 Mixed Signal DSP Solutions, July 2000. 123 Texas Instruments User's Guide, TLK1501 Serdes EVM Kit Setup and Usage, Mixed Signal Products, June 2000. Date Considered: Examiner: *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609, draw line through citation if not in conformance and not considered. Include copy of

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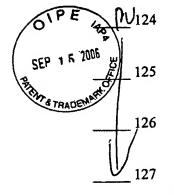
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While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

Examiners will consider all citations submitted in conformance with 37 C.F.R. § 1.98 and MPEP Sec. 609 and place their initials adjacent the citations in the spaces provided on this form. Examiners will also initial citations not in conformance with the guidelines which may have been considered. A reference may be considered by the Examiner for any reason whether or not the citation is in full conformance with the guidelines. A line will be drawn through a citation if it is not in conformance with the guidelines AND has not been considered. A copy of the submitted form, as reviewed by the Examiner, will be returned to the applicant with the next communication. The original of the form will be entered into the application file.

Each citation initialed by the Examiner will be printed on the issued patent in the same manner as references cited by the Examiner on Form PTO-892.

The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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